

REMARKS

Applicants respectfully requests reconsideration of the above referenced patent application in view of the amendments and remarks set forth herein, and respectfully request that the Examiner withdraw all rejections. Claim 1 has been amended. No claims have been canceled. No claims have been added. Thus, claims 1-12 are pending.

35 U.S.C. §102 Rejections

35 U.S.C. §102(b) Rejection over *Porterfield*

The Office Action rejects claims 1, 2 and 6-10 under §102(b) as being anticipated by *Porterfield*, US Patent Application Publication No. US 2002/0016862 A1 (*Porterfield*). The Office Action alleges that *Porterfield* discloses, *inter alia*, determining a resource allocation scheme to support resource requests of peripheral devices that consumes a minimum amount of address space. To overcome a 35 U.S.C. §102(b) rejection, Applicants may either demonstrate that the cited document fails to teach one limitation in the rejected claim, or add such a limitation to the claim by appropriate amendment, M.P.E.P. § 2131. For at least the following reasons Applicants traverse the above rejection.

Currently amended independent claim 1 recites in a salient portion (emphasis added):

“...**determining a resource allocation scheme** to support the resource requests of the peripheral devices, the scheme to **minimize an amount of allocated address space**;...”

The amendments are supported in the original disclosure at least by paragraphs [0039] and [0044]. Applicants respectfully submit that the rejected claims are patentable over *Porterfield* at least based on independent claim 1, as amended. Specifically, *Porterfield* does not disclose determination of a resource allocation scheme to support the resource requests of peripheral devices, the scheme to minimize an amount of allocated address space.

To support the contention that *Porterfield* anticipates this limitation, the Office Action points to paragraphs [0021]-[0022] of the reference, which describe a processor writing a minimum memory address into a data register. However, the minimum memory address in paragraph [0021] is disclosed to be nothing more than a minimum memory address of a memory address range, i.e. an indication of where a block of memory begins. This minimum memory address is **not** disclosed in any way as **minimizing an amount of allocated address space**. Applicants submit that **nothing** in the cited passages or elsewhere in *Porterfield* discloses a determination of a resource allocation scheme to support the resource requests of peripheral devices, the scheme to minimize an amount of allocated address space.

Accordingly, amended independent claim 1 contains at least one limitation not found in *Porterfield*, and is therefore patentable over *Porterfield*. Furthermore, in depending directly or indirectly from independent claim 1, each of dependent claims 2

and 6-10 incorporate at least one limitation not found in *Porterfield*. Therefore, the applicants request that the rejection of claims 1, 2 and 6-10 under 35 U.S.C. §102(b) based on *Porterfield* be withdrawn.

35 U.S.C. §103(a) Rejections

35 U.S.C. §103(a) Rejection over *Porterfield* in view of *Arimilli*

The Office Action rejects claims 11 and 12 under §103(a) as being unpatentable over *Porterfield*, based on the rejection of claim 1, in view of *Arimilli* et al., US Patent Application Publication No. US 2004/0215864 A1. Specifically, *Arimilli* is alleged to disclose the distinguishing limitations of claims 11 and 12 regarding allocation and dynamic reallocation of address space for hot-plug devices. To overcome a rejection under 35 U.S.C. §103(a), Applicants may show that there is one limitation of the claim which is not taught or suggested by any combination of the cited references, or alternatively add such a limitation by appropriate amendment, M.P.E.P. § 2143.03. For at least the following reasons Applicants traverse these rejections.

As discussed previously, *Porterfield* fails to disclose at least one limitation of currently amended independent claim 1, i.e. determination of a resource allocation scheme to support the resource requests of peripheral devices, the scheme to minimize an amount of allocated address space. The Office Action does not offer *Arimilli* as further suggesting, nor does it teach or suggest, this limitation in any way. In depending directly or indirectly from independent claim 1, each of dependent claims 11 and 12 incorporate at least one limitation not taught or suggested by any combination of *Porterfield* and

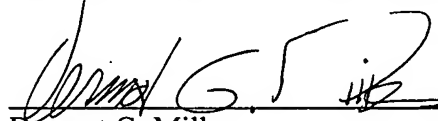
Arimilli. Accordingly, the claims are patentable and Applicants request that the rejection of claims 11 and 12 under §103(a) based on *Porterfield* and *Arimilli* be withdrawn.

CONCLUSION

For at least the foregoing reasons, Applicants submit that the objections and rejections have been overcome. Therefore, claims 1-12 are in condition for allowance and such action is earnestly solicited. The Examiner is respectfully requested to contact the undersigned by telephone if such contact would further the examination of the present application. Please charge any shortages and credit any overcharges to our Deposit Account No. 02-2666.

Respectfully submitted,
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